

Notice of References Cited	Application/Control No. 10/657,609		Applicant(s)/Patent Under Reexamination MCGUINNESS ET AL.	
	Examiner Nghia M. Doan		Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,351,841	02-2002	Tickle, Andrew C.	716/8
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chou et al., A Multiple-row transistor placement system for full custom design, April 2005, IEEE, Pages 136-139
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.